

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
PATENT APPLICATION**

Applicant: Jeffrey S. Brown et al. Art Unit: 2814
Serial No.: 10/064,869 Examiner: Howard Weiss
Filed: June 6, 2003 Docket: BUR920010223US1
Title: DRAM CELL WITH ENHANCED SER IMMUNITY

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.56, 1.97, 1.98**

Honorable Commissioner of Patents and Trademarks
Washington, D. C. 20231

Sir:

Applicants submit herewith form PTO-1449, listing patents, publications, or other information of which they are aware which they believe may be material to patentability pursuant to 37 CFR 1.56(b), and in respect of which there may be a duty to disclose under 37 CFR 1.56(a), together with legible copies of the patents, publications, or other information listed. Any fees required in entering this response may be charged to Applicant's deposit account, 09-0456.

While the items submitted with this Information Disclosure Statement may be material to patentability pursuant to 37 CFR 1.56, in accordance with 37 CFR 1.97(h) it shall not be construed to be an admission that any patent, publication, or other information cited is "prior art" or is material to patentability for this invention unless specifically designated as such. In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other information material to patentability, as defined in 37 CFR 1.56(b), exists.

Respectfully submitted,

Date: 6/6/2003

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INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Docket Number (Optional)
BUR92001022. 1

Application Number
10/064,869

Applicant(s)
Jeffrey S. Brown, et al.

Filing Date
08/26/2002

Group Art Unit
2814

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
		FR2595160 - A1	09/04/1987	France				<input checked="" type="checkbox"/>

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		IBM Technical Disclosure Bulletin, Vol 31, No. 7, December 1988, pp. 409-417; Hisamoto et al., "A Fully Depleted Lean-Channel Transistor (DELTA), CH2637-7/89/0000-0833, 1989 IEEE, IEDM 89-833-836; Choi, et al., "Ultra-thin Body SOI MOSFET for Deep-sub-tenth Micron Era", 0-7803-5410-9/99, 1999 IEEE, IEDM 99-919-921

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Notice of References Cited

Application/Control No.

10/064,869

Applicant(s)/Patent Under
Reexamination
BROWN ET AL.

Examiner

Howard Weiss

Art Unit

2814

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	A	US-6,198,151	03-2001	Wada	257/520
	B	US-5,943,279	08-1999	Wada	365/205
	C	US-5,329,479 ✓	07-1994	Ota et al.	365/149
	D	US-5,241,211	08-1993	Tashiro	257/506
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	F	US-4,641,165 ✓	02-1987	Iizuka et al.	257/311
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Choi, Y-K. et al., "30 nm ultra-thin-body SOI MOSFET with selective deposited Ge raised S/D", June 2000, 58th Dev. Res. Conf. (DRC), pp. 23-4
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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Art Unit

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	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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	N					
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